

PRODUCT SPECIFICATION

6221M-PUC

Wi-Fi Dual-band 1T1R 11a/b/g/n/ac + Bluetooth 5.0

Combo Module

Version:v2.0



6221M-PUC Module Datasheet

Ordering Information	Part NO.	Description
	FG6221MPUC-02	RTL8821CE-VC-CG, a/b/g/n/ac Wi-Fi, 1T1R, BT5.0, 22.0*30.0mm, PCIe,USB,PCB V2.0
	FG6221MPUC-03	RTL8821CE-VC-CG, a/b/g/n/ac Wi-Fi, 1T1R, BT5.0, 22.0*30.0mm, PCIe,USB,PCB V2.0 (不支持 5.47~5.725GHz)

Customer: _____

Customer P/N: _____

Signature: _____

Date: _____

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Revision History

[illegible]

1. General Description

1.1 Introduction

Fn-Link Technology would like to announce a low-cost and low-power consumption module which has all of the Wi-Fi functionalities. It is a highly-integrated IEEE 802.11 a/b/g/n/ac MAC/Baseband/RF WLAN single chip. For Wireless LAN(WLAN)operation. The integrated module provides PCI-e interface for Wi-Fi . The module provides simple legacy and 20MHz/40MHz/80MHz co-existence mechanisms to ensure backward and network compatibility

The wireless module complies with IEEE 802.11 a/b/g/n/ac 1x1 SISO standard and it can achieve up to a speed of 433Mbps to connect the wireless LAN. The integrated module provides PCI-e interface for Wi-Fi, USB interface for Bluetooth.

This compact module is a total solution for a combination of Wi-Fi and Bluetooth V5.0 technologies. The module is specifically developed for all portable devices.

1.2 Description

Model Name	6221M-PUC
Product Description	Support Wi-Fi/Bluetooth functionalities
Dimension	L x W x H: 30x 22x 2.2 (typical) mm
Wi-Fi Interface	Support PCI-e
BT Interface	USB
OS supported	Android /Linux/ Win CE /XP/WIN
Operating temperature	0°C to 70°C
Storage temperature	-55°C to 125°C

2. Features

General

- Highly integrated wireless local area network (WLAN) system-on-chip (SoC) for 5 GHz 802.11ac, or 2.4G/5G 802.11n WLAN applications
- Supports 20/40MHz at 2.4GHz and supports 20/40/80MHz at 5GHz

Host Interface

- Supports low power PCI-e interface for WLAN and USB interface for Bluetooth
- Supports WLAN-Bluetooth coexistence and ISM-LTE coexistence

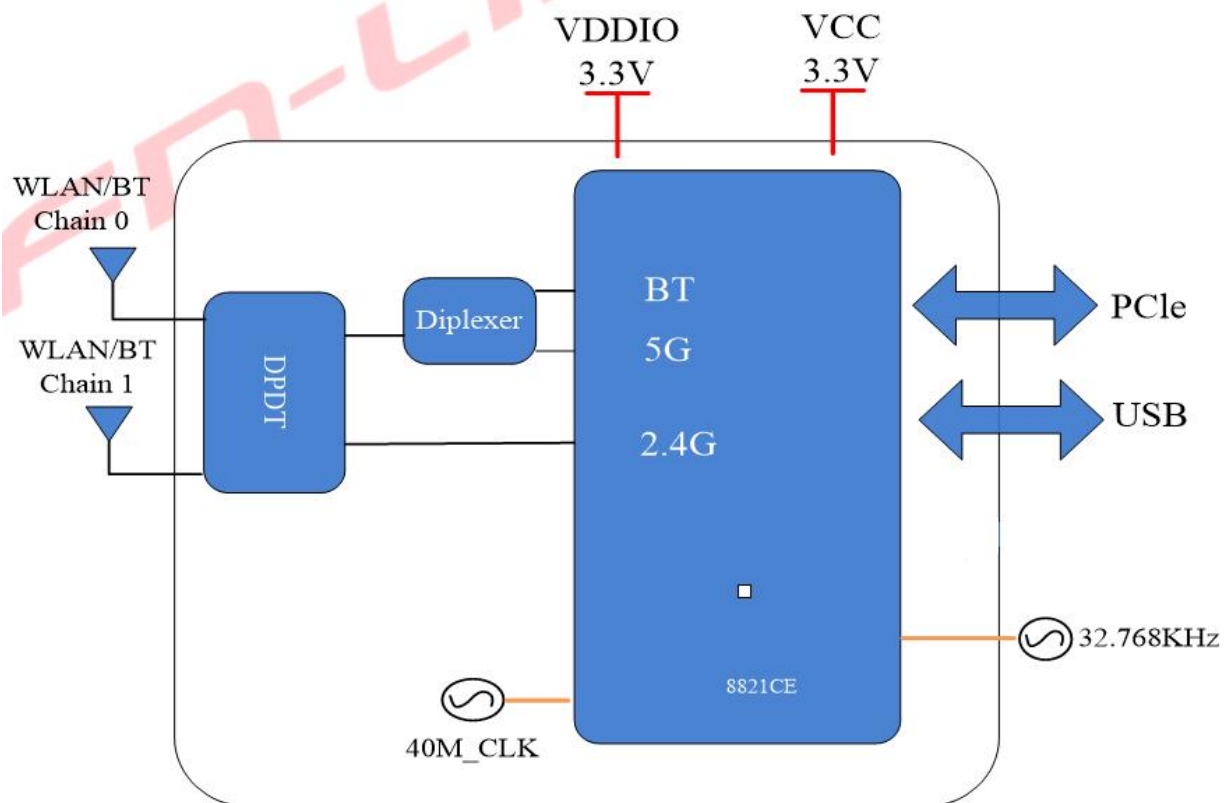
Bluetooth Features

- Supports Bluetooth V5.0
- Support bluetooth 4.2system, compatible with BT v2.1+EDR
- Supports Bluetooth for class1 and class2 power level transmissions without requiring an external PA.
- BT host digital interface:
 - USB1.1

Software Package Feature:

- IEEE 802.11 b/g/n/ac WiFi compliant
- 802.1x, WEP, WPA TKIP and WPA2 AES/Mixed mode for PSK and TLS (Radius)
- WPS - PIN and PBC Methods
- IEEE 802.11b/g/n/ac Client mode
- Support wireless security for WEP, WPA TKIP and WPA2 AES PSK Support site survey scan and manual connect
- Support WPA/WPA2 TLS client
- Support power saving mode
- Soft AP mode
- WiFi-Direct

3. Block Diagram



4. General Specification

4.1 2.4GHz WI-FI Specification

Feature	Description		
WLAN Standard	IEEE 802.11 b/g/n Wi-Fi compliant		
Frequency Range	2.400 GHz ~ 2.4835 GHz (2.4 GHz ISM Band)		
Number of Channels	2.4GHz: Ch1 ~ Ch14		
Test Items	Typical Value		EVM
Output Power	802.11b /11Mbps	17dBm ± 2 dB	EVM ≤ -9dB
	802.11g /54Mbps	15dBm ± 2 dB	EVM ≤ -25dB
	802.11n /MCS7	14dBm ± 2 dB	EVM ≤ -28dB
Spectrum Mask	Meet with IEEE standard		
Freq. Tolerance	± 20ppm		
Receive Sensitivity (11b,20MHz) @8% PER	- 1Mbps	PER @ -95 dBm	≤83
	- 11Mbps	PER @ -87 dBm	≤-76
Receive Sensitivity (11g,20MHz) @10% PER	- 6Mbps	PER @ -88 dBm	≤-85
	- 54Mbps	PER @ -72 dBm	≤-68

Receive Sensitivity	- MCS=0	PER @ -91 dBm	≤-85
(11n,20MHz) @10% PER	- MCS=7	PER @ -70 dBm	≤-67
Receive Sensitivity	- MCS=0,	PER @ -83 dBm	≤-82
(11n,40MHz) @10% PER	- MCS=7,	PER @ -66 dBm	≤-64
Maximum Input Level	802.11b : -10 dBm		
	802.11g/n : -20 dBm		
Antenna Reference	Small antennas with 0~2 dBi peak gain		

4.2 5GHz WI-FI Specification

Conditions : VBAT=3.3V ; VDDIO=3.3V ; Temp:25°C

Feature	Description		
WLAN Standard	IEEE 802.11a/n/ac, Wi-Fi compliant		
Frequency Range	5.150 GHz ~ 5.850 GHz (5.0 GHz ISM Band)		
Number of Channels	5.0GHz: Please see the table1		
Output Power	802.11a /54Mbps	13 dBm ± 2 dB	EVM ≤ -25dB
	802.11n /MCS7	12 dBm ± 2 dB	EVM ≤ -28dB
	802.11ac /MCS9	11 dBm ± 2 dB	EVM ≤ -32dB
Test Items	Test Value		Standard Value
Receive Sensitivity	- 6Mbps	PER ≤ -87 dBm	≤-85
(11a, 20MHz) @10% PER	- 54Mbps	PER ≤ -71 dBm	≤-68
Receive Sensitivity	- MCS=0	PER ≤ -86 dBm	≤-85
(11n,20MHz) @10% PER	- MCS=7	PER ≤ -68 dBm	≤-67
Receive Sensitivity	- MCS=0	PER ≤ -83 dBm	≤-82
(11n,40MHz) @10% PER	- MCS=7	PER ≤ -65 dBm	≤-64
Receive Sensitivity	- MCS=0, NSS1	PER ≤ -83 dBm	≤-82
(11ac,20MHz) @10% PER	- MCS=8, NSS1	PER ≤ -64 dBm	≤-60
Receive Sensitivity	- MCS=0, NSS1	PER ≤ -82 dBm	≤-79
(11ac,40MHz) @10% PER	- MCS=9, NSS1	PER ≤ -60 dBm	≤-55
Receive Sensitivity	- MCS=0, NSS1	PER ≤ -81 dBm	≤-79
(11ac,80MHz) @10% PER	- MCS=9, NSS1	PER ≤ -56 dBm	≤-54
Maximum Input Level	802.11a/n : -30 dBm		
Antenna Reference	Small antennas with 0~2 dBi peak gain		

15GHz(20MHz) Channel table

Band range	Operating Channel Numbers	Channel center frequencies(MHz)
5150MHz~5250MHz	36	5180

	40	5200
	44	5220
	48	5240
5250MHz~5350MHz	52	5260
	56	5280
	60	5300
	64	5320
5470MHz~5725MHz	100	5500
	104	5520
	108	5540
	112	5560
	116	5580
	120	5600
	124	5620
	128	5640
	132	5660
	136	5680
5725MHz~5850MHz	140	5700
	149	5745
	153	5765
	157	5785
	161	5805
	165	5825

4.3 Bluetooth Specification

Feature	Description		
General Specification			
Bluetooth Standard	Bluetooth V5.0 of 1, 2 and 3 Mbps.		
Host Interface	USB		
Antenna Reference	Small antennas with 0~2 dBi peak gain		
Frequency Band	2400 MHz ~ 2483.5 MHz		
Number of Channels	79 channels for classic,40 channels for BLE		
Modulation	GFSK, $\pi/4$ -DQPSK,8DPSK		
RF Specification			
	Min(dBm)	Typical(dBm)	Max(dBm)
Output Power (Class 1)	2	4	6
Sensitivity @ BER=0.1%		-92	

for GFSK (1Mbps)			
Sensitivity @ BER=0.01% for $\pi/4$ -DQPSK (2Mbps)		-92	
Sensitivity @ BER=0.01% for 8DPSK (3Mbps)		-85	
Maximum Input Level	GFSK (1Mbps):-20dBm		
	$\pi/4$ -DQPSK (2Mbps) :-20dBm		
	8DPSK (3Mbps) :-20dBm		

5. ID setting information

WI-FI

Vendor ID	10EC
Product ID	C821

6. Pin Definition

6.1 Pin Outline

< TOP VIEW >

Pin	Signal	Signal	Pin
74	3.3 V	GND	75
72	3.3 V	RESERVED/REFCLKn1	73
70	UIM_POWER_SRC/GPIO_1/PEWAKE1#	RESERVED/REFCLKp1	71
68	UIM_POWER_SNK/CLKREQ1#	GND	69
66	UIM_SWP/PERST1#	RESERVED/PETn1	67
64	RESERVED	RESERVED/PETp1	65
62	ALERT# (O)(0/1.8 V)	GND	63
60	I2C_CLK (I)(0/1.8 V)	RESERVED/PERn1	61
58	I2C_DATA (I/O)(0/1.8 V)	RESERVED/PERp1	59
56	W_DISABLE1# (I)(0/3.3V)	GND	57
54	W_DISABLE2# (I)(0/3.3V)	PEWAKE0# (I/O)(0/3.3V)	55
52	PERST0# (I)(0/3.3V)	CLKREQ0# (I/O)(0/3.3V)	53
50	SUSCLK(32kHz) (I)(0/3.3V)	GND	51
48	COEX_RXD (I)(0/1.8V)	REFCLKn0	49
46	COEX_TXD (O)(0/1.8V)	REFCLKp0	47
44	COEX3 (I/O)(0/1.8V)	GND	45
42	VENDOR DEFINED	PETn0	43
40	VENDOR DEFINED	PETp0	41
38	VENDOR DEFINED	GND	39
36	UART CTS (I)(0/1.8V)	PERn0	37
34	UART RTS (O)(0/1.8V)	PERp0	35
32	UART RXD (I)(0/1.8V)	GND	33
	ADD-IN CARD KEY E	ADD-IN CARD KEY E	
	ADD-IN CARD KEY E	ADD-IN CARD KEY E	
	ADD-IN CARD KEY E	ADD-IN CARD KEY E	
	ADD-IN CARD KEY E	ADD-IN CARD KEY E	
	ADD-IN CARD KEY E	ADD-IN CARD KEY E	
22	UART TXD (O)(0/1.8V)	SDIO RESET#/Tx_BLANKING (I)(0/1.8V)	23
20	UART WAKE# (O)(0/3.3V)	SDIO WAKE# (O)(0/1.8V)	21
18	GND	SDIO DATA3 (I/O)(0/1.8V)	19
16	LED_2# (O)(OD)	SDIO DATA2 (I/O)(0/1.8V)	17
	ADD-IN CARD KEY A	ADD-IN CARD KEY A	
	ADD-IN CARD KEY A	ADD-IN CARD KEY A	
	ADD-IN CARD KEY A	ADD-IN CARD KEY A	
	ADD-IN CARD KEY A	ADD-IN CARD KEY A	
6	LED_1# (O)(OD)	GND	7
4	3.3 V	USB_D-	5
2	3.3 V	USB_D+	3
		GND	1

6.2 Pin Definition details

NO	Name	Type	Description	Voltage
1	GND	-	Ground	
3	USB_D+	I/O	USB differential line for BT	
5	USB_D-	I/O		
7	GND	-	Ground	

9	NC	-	No connect	
17	NC	-	No connect	
19	NC	-	No connect	
21	NC	-	No connect	
23	NC	-	No connect	
33	GND	-	Ground	
35	PERP0	I	PCIe RX differential signals	
37	PERN0	I		
39	GND	-	Ground	
41	PETP0	O	PCIe TX differential signals	
43	PETN0	O		
45	GND	-	Ground	
47	REFCLKP0	I	PCIe clock differential input signal	
49	REFCLKN0	I		
51	GND		Ground	
53	CLKREQ0	O	PCIe reference clock request signal, open drain, active low	3.3V
55	PEWAKE0	O	PCIe wake up host, open drain, active low	3.3V
57	GND	-	Ground	
59	NC	-	NC	
61	NC	-	NC	
63	GND	-	Ground	
65	NC	-	NC	
67	NC	-	NC	
69	GND9	-	Ground	
71	RESERVED	-	NC or GPIO2 Reserved	3.3V
73	RESERVED	-	NC or GPIO3 Reserved	3.3V
75	GND10	-	Ground	

Bottom side

NO	Name	Type	Description	Voltage
2	3_3V	P	Power supply	3.3V
4	3_3V	P	Power supply	3.3V
6	LED_1#	O	WLAN LED signal	3.3V
8	NC	-	NC	

10	NC	-	NC	
12	NC	-	NC	
14	NC	-	NC	
16	LED_2#	O	BT LED signal	3.3V
18	GND	-	NC	
20	NC	-	NC	
22	NC	-	NC	
32	NC	-	NC	
34	NC	-	NC	
36	NC	-	NC	
38	NC	-	NC	
40	NC	-	NC	
42	NC	-	NC	
44	COEX3	I/O	LTE coexistence signal	3.3V
46	LTE_COEX_TXD	O	LTE coexistence signal	3.3V
48	LTE_COEX_RXD	I	LTE coexistence signal	3.3V
50	SUSCLK	I	Sleep clock input	3.3V
52	PERST0	I	PCIe reset signal, active low	3.3V
54	BT_DIS_N	I	Bluetooth enable signal, pull low to disable BT function, default high.	3.3V
56	WL_DIS_N	I	WLAN enable signal, pull low to disable BT function, default high.	3.3V
58	NC	-	NC	
60	NC	-	NC	
62	NC	-	NC	
64	NC	-	NC	
66	NC	-	NC	
68	NC	-	NC	
70	NC	-	NC	
72	NC	-	NC	
74	NC	-	NC	

P:POWER I:INPUT O:OUTPUT PD: PULL-DOWN

7. Electrical Specifications

7.1 Power Supply DC Characteristics

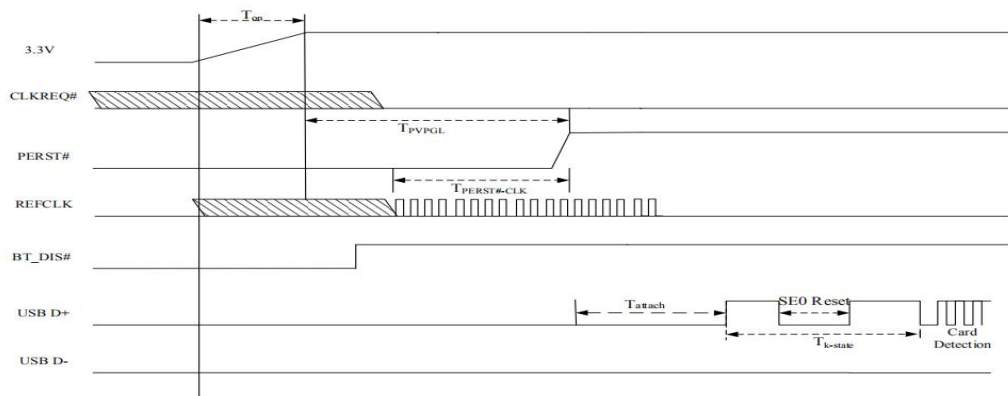
	MIN	TYP	MAX	Unit
Operating Temperature	0	25	70	deg.C
VCC33	3.0	3.3	3.6	V
VDDIO	3.0	3.3	3.6	V

7.2 Power Consumption

Power Consumption		VCC33 = 3.3V(Unit:mA)
	Wi-Fi on Mode	25
	TX (2.4G HT40)	275
	RX (2.4G HT40)	134
	TX (5G vHT80)	264
	RX (5G vHT80)	139
	BT on	23

7.3 Interface Circuit time series

7.3.1 PCIe Bus during Power On Sequence

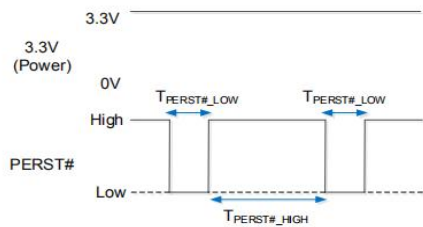


- Ton: The main power ramp up duration;
- TPVPGL: Power valid to PERST# input inactive;
- TPERST#-CLK: Reference clock stable before PERST# inactive;
- Tattach: The interval to turn on BT after PERST# de-asserted;
- Tk-state: the duration from resister attached to USB host starting card detection procedure;

The typical timing range

Symbol	Unit	Min	Typical	Max
T _{on}	ms	0.5	1.5	5
T _{PVPGL}	ms	Implementation specific;recommended 50ms		--
T _{PERST#-CLK}	us	100		--
T _{attach}	ms	0.5	2	5
T _{k-state}	ms	50	250	--

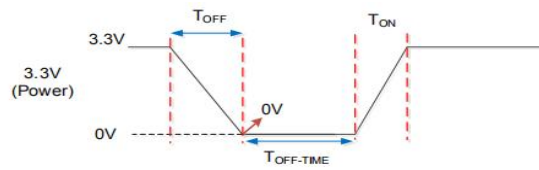
7.3.2 PCIe PERST# Timing Sequence



RTL8821CE-CG PCIE PERST# Timing Parameters

	Min	Typical	Max	Unit	Description
T _{PERST#_LOW}	6	10	X	ms	PERST# low duration
T _{PERST#_HIGH}	400	500	X	ms	PERST# high duration

7.3.3 Power Off Sequence



RTL8821CE-CG Power Off Sequence
RTL8821CE-CG Power Off Timing Parameters

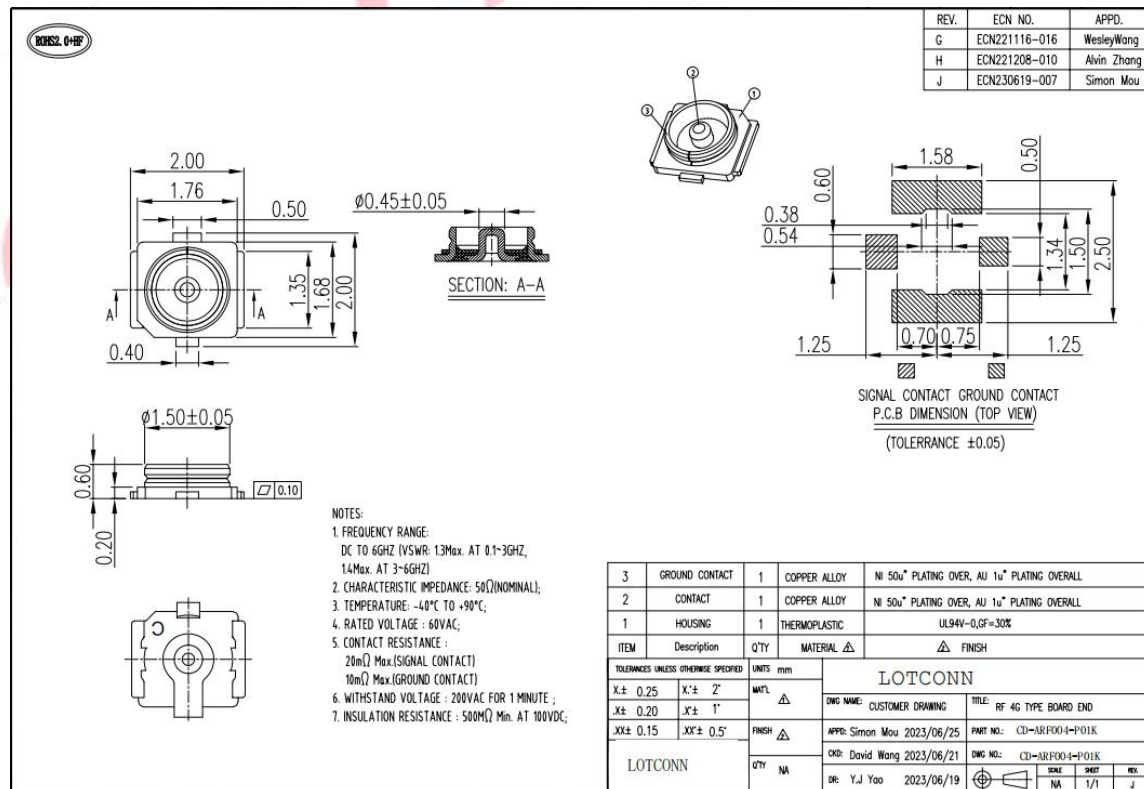
Symbol	Unit	Min	Typical	Max
T_{OFF}	ms	5	20	50
$T_{OFF-TIME}$	ms	500	--	--
T_{ON}	ms	0.5	1.5	5

7.4 M.2 Connector

The module is standard M.2 2230 Key A and Key E single sided module.

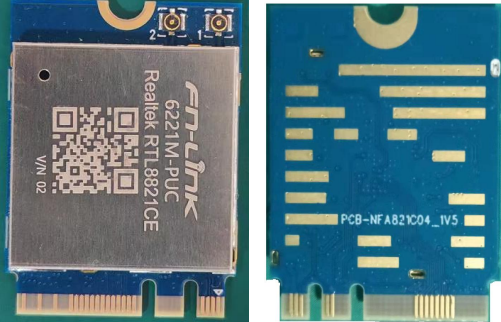
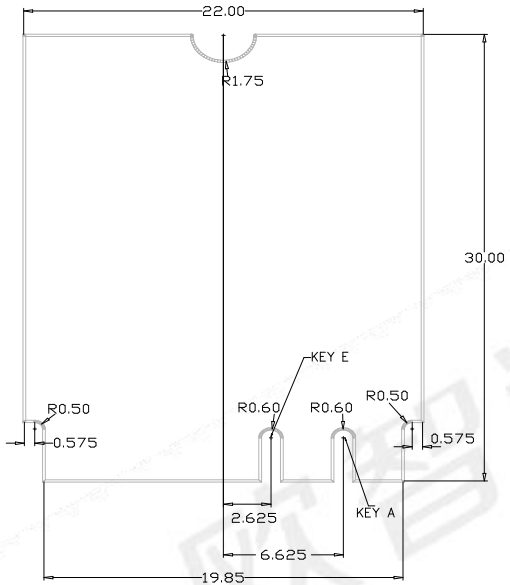
It complies with the standard M.2 2230 Key A and Key E slot.

7.5 RF Connector Specification



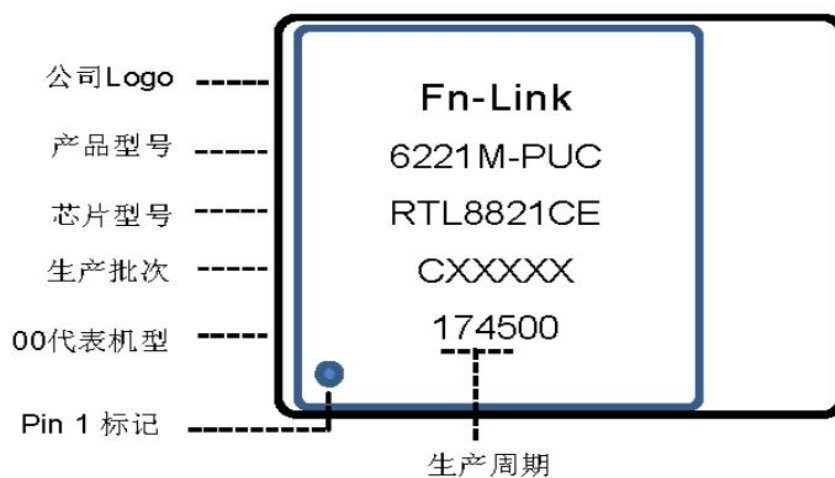
8. Size reference

8.1 Module Picture

L x W : 30 x 22 (+0.3/-0.1) mm 	
H: 2.2 (±0.2) mm	
Weight	2.6g

8.2 Marking Description

< TOP VIEW >



模组尺寸: 22x30mm
屏蔽盖尺寸: 21.11x21.08mm

datacode '03' is P/N: FG6221MPUC-03



备注:

二维码内容: 112233445566;FG6221MPUC-02

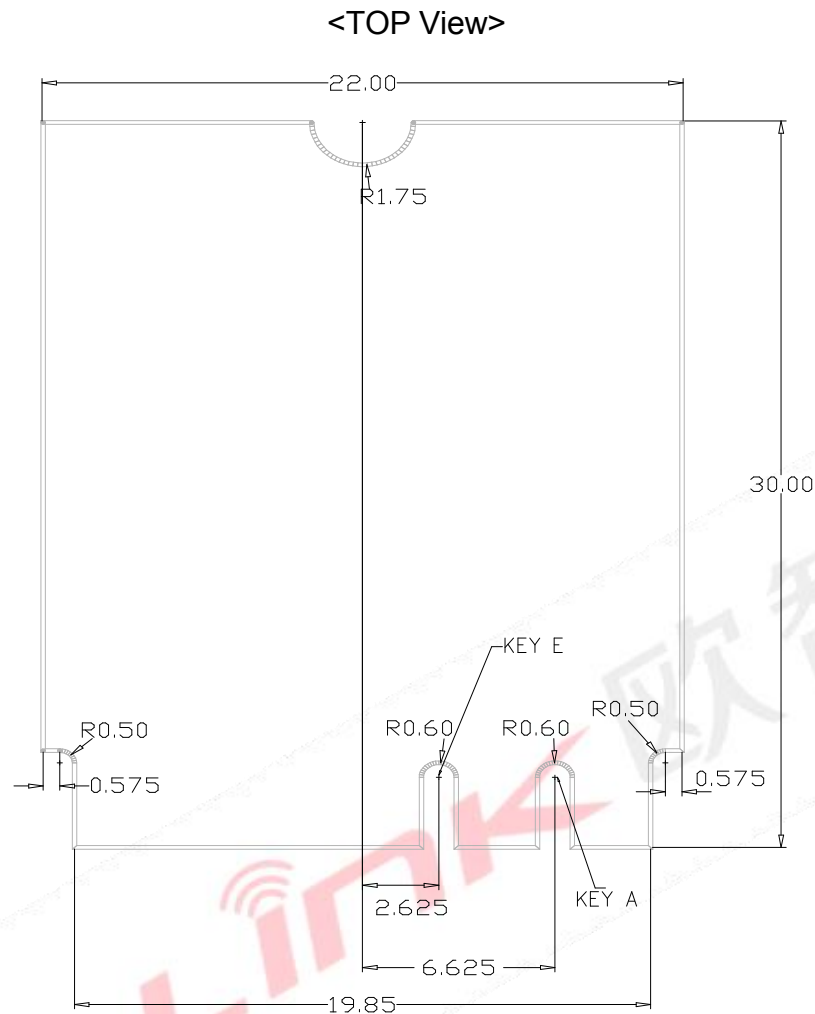
1. Fn-Link 商标 logo
2. 机型: 6221M-PUC 不变。
3. 芯片: Realtek RTL8821CE 不变。
4. 二维码: 编码规则为 “wifi mac 地址;成品料号”

例如: 112233445566;FG6221MPUC-02 (MAC 地址以实际为准)

——BT MAC 在 WIFI MAC 基础上+1 (WIFI 地址需跳 1, 不能与 BT 地址重复), 二维码中不显示此内容。

5.V/N 02 (V/N 到 02 之间为两个空格, 02 为成品料号后缀, 与二维码右对齐。)

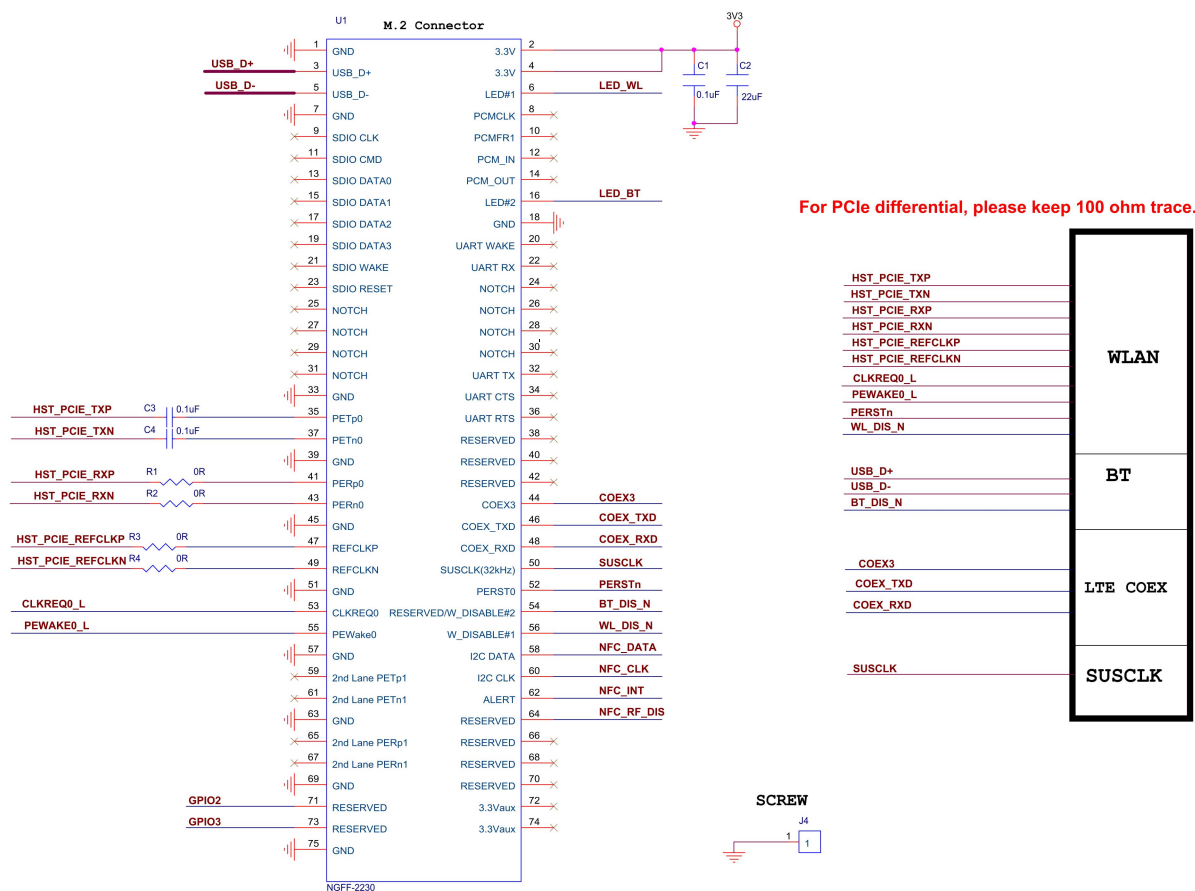
8.3 Physical Dimensions



9. The Key Material List

Item	Part Name	Description	Manufacturer
1	Inductor	2016 2.2uH,±20%	Sunlord, Ceaiya, Cenker
2	Diplexer	1005 Dual-band, dual-mode 2.4GHz/5GHz WLAN	Glead, Walsin, ACX, Murata, MAG.LAYERS, ftrgroup
3	Crystal	2520 40MHz	ECEC, TKD, Hosonic, JWT, TXC
4	Chipset	RTL8821CE	Realtek
5	PCB	FR4, 4 LAYER	XY-PCB, GDKX, Sunlord, SLPCB
6	ipex	4 代天线座, CD-ARF004-P01K	创迪尔

10. Reference Design



Note:

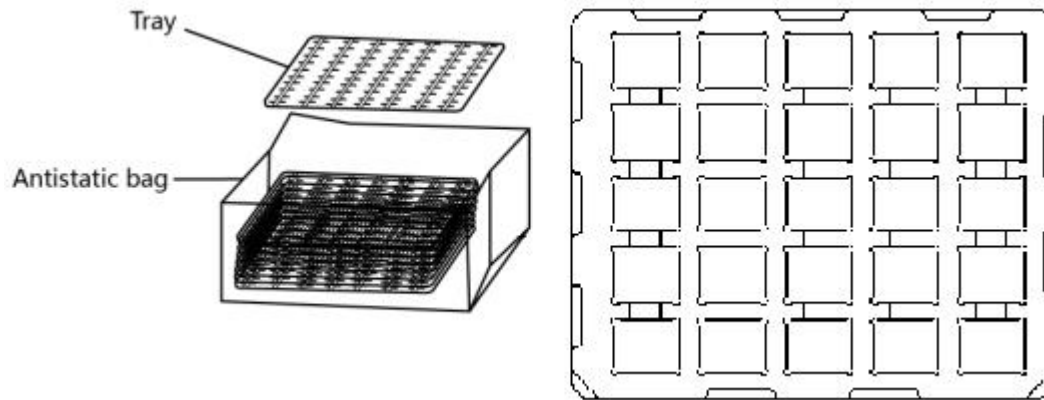
- Both of the 2 ANT's are all support 2.4G/5G/BT function.
- 6221M-PUC antenna port is control by driver if diversity function is enabled.
- C1, C2 placed close to module side.
- PCIe differential keep 100 ohm trace.
- USB differential keep 90 ohm trace.

11. RoHS compliance

All hardware components are fully compliant with EU RoHS directive

12. Package

12.1 Tray



13. Moisture sensitivity

The Modules is a Moisture Sensitive Device level 3, in according with standard IPC/JEDEC J-STD-020, take care

all the relatives requirements for using this kind of components.

Moreover, the customer has to take care of the following conditions:

- a) Calculated shelf life in sealed bag: 12 months at $<40^{\circ}\text{C}$ and $<90\%$ relative humidity (RH)
- b) Environmental condition during the production: 30°C / 60% RH according to IPC/JEDEC J-STD-033A paragraph 5
- c) The maximum time between the opening of the sealed bag and the reflow process must be 168 hours if condition b) "IPC/JEDEC J-STD-033A paragraph 5.2" is respected
- d) Baking is required if conditions b) or c) are not respected
- e) Baking is required if the humidity indicator inside the bag indicates 10% RH or more